



PATENT APPLICATION  
Do. No. 1482-129

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Barrie Gilbert

Serial No.: 09/675,902

Examiner: Tung X. Nguyen

Filed: September 28, 2000

Group Art Unit: 2829

For: GAIN AND PHASE DETECTOR HAVING DUAL LOGARITHMIC  
AMPLIFIERS

Date: March 19, 2003

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U.S. PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub-</u> <u>Class</u>
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FOREIGN PATENT DOCUMENTS

<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>
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OTHER DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>(Including Author, Title, Date, Pertinent Pages, etc.)</u>
<u>DN</u>	<u>    </u>	Monolithic Logarithmic Amplifiers article by Barrie Gilbert, Analog Devices, Inc., August 1994, pp. 1-122
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Examiner:                     

Date Considered: 5/16/03